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Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000, provides for continued examination of an utility or plant application filed on or after June 8, 1995.

See The American Inventors Protection Act of 1999 (AIPA).

Application Number	10/081,818
Filing Date	February 20, 2002
First Named Inventor	Jerome M. Eldridge
Group Art Unit	2818
Examiner Name	Tu-Tu Ho
Attorney Docket Number	1303.045US1
Customer No.	21186

This is a Request for Continued Examination (RCE) under 37 CFR § 1.114 of the above-identified application entitled ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS.

Submission required under 37 C.F.R. § 1.114

- 1. _ Consider the amendment(s)/reply under 37 C.F.R. § 1.116 previously filed on .
- 2. _ Consider the arguments in the Appeal Brief or Reply Brief previously filed on .
- 3. _ An Amendment Under 37 CFR § 1.116 (pages) is enclosed.
- 4. _ A new power of attorney (pages) is enclosed.
- 5. X An Information Disclosure Statement is enclosed (2 pages) with Form 1449 (1 pg.).
- 6. \underline{X} A check in the amount of \$790.00 is attached to pay the RCE filing fee required under C.F.R. § 1.17(e).
- 7. X The Commissioner is hereby authorized to credit overpayments or charge any fees set forth in 37 CFR §§ 1.16 through 1.18 to Deposit Account No. 19-0743.
- 8. __ A Petition for Extension of Time in the prior application (pages) is enclosed along with a check in the amount of to pay the extension fee.
- 9. X Others: Communication Concerning Related Applications (2 pgs.).
 Communication Concerning Application Title (1 pg.).

<u>SCHWE</u>	GMAN, LU	INDBERG,	WOESSNER	& KLUTH.	P.A.
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Attn-Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this _____ day of March, 2005.

FACIA LEE

Signature

<u>S/N 10/081,818</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Jerome M. Eldridge et al.

Examiner: Tu-Tu Ho

Serial No.:

10/081,818

Group Art Unit: 2818

Filed:

February 20, 2002

Docket: 1303.045US1

Title:

ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW

ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS

COMMUNICATION CONCERNING APPLICATION TITLE

Mail Stop RCE
Commissioner for F

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Applicant notes that the Notice of Allowance mailed February 22, 2005 contains an error in the title of the application. Specifically, the Notice shows the second to last word in the title as "INTERPLOY". This word should read "INTERPOLY". This amendment to the title was made by Applicant in the amendment filed April 2, 2003. Applicant respectfully requests that the Office update its records to reflect this amendment to the title.

It is believed that no fee is required with this communication. If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

JEROME M. ELDRIDGE ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

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Minneapolis, MN 55402

(612) 349-9587

Date 1 March 105

Timothy B Clise

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CACIA LEE

Signature Lee

<u>S/N 10/081,818</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jerome M. Eldridge et al.

Examiner: Tu-Tu Ho

erial No.: 10/081,818

Group Art Unit: 2818

ed: February 20, 2002

Docket: 1303.045US1

ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW

ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

Serial/Patent No. 10/929916	Filing Date August 30, 2004	Attorney Docket 1303.035US2	Title PROGRAMMABLE ARRAY LOGIC OR MEMORY WITH P-CHANNEL DEVICES AND ASYMMETRICAL TUNNEL BARRIERS
10/931704	September 1, 2004	1303.014US2	FLASH MEMORY WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
10/929986	August 30, 2004	1303.045US2	ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS
10/931711	September 1, 2004	1303.029US2	PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
11/062543	February 22, 2005	1303.028US2	SRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS
10/931540	August 31, 2004	1303.020US2	PROGRAMMABLE ARRAY LOGIC OR MEMORY DEVICES WITH

COMMUNICATION CONCERNING RELATED APPLICATIONS

Serial Number: 10/081,818

Filing Date: February 20, 2002

Title: ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS

ASYMMETRICAL TUNNEL **BARRIERS**

February 23, 2005 1303.132US1

GERMANIUM-SILICON-CARBIDE FLOATING GATES IN MEMORIES

Page 2

Dkt: 1303.045US1

Respectfully submitted,

JEROME M. ELDRIDGE ET AL.

By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. P.O. Box 2938

Minneapolis, MN 55402

(612) 349-9587

Date 9 March 105

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Paria Lee